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Dkt. 2271/71525

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Katsuhiko AISU

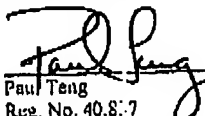
Serial No.: 10/763,343

Group Art Unit: 2817

Date Filed: January 23, 2004

Examiner: Krista M. Flanagan

For: SEMICONDUCTOR INTEGRATED CIRCUIT AND AMPLIFIER FOR
SUPPRESSING POP SOUND WHILE MINIMIZING VOLTAGE TRANSITION
SETTLING TIME

I hereby certify that this correspondence is being transmitted by facsimile transmission to the U.S. Patent and Trademark Office at (571) 273-8300.	
 Paul Teng Reg. No. 40,817	August 24, 2007 Date

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Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
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AMENDMENT

Sir:

This Amendment is submitted in response to the Office Action dated June 22, 2007 in connection with the above-identified application.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Drawings begin on page 3 of this paper.

Remarks begin on page 4 of this paper.

Replacement sheets of drawings for Figures 7, 8A and 8B are attached hereto as **Exhibit**

A.